

Instruction set architecture

This chapter describes the instruction set architecture of YiffCore

- [Instruction listing](#)

Instruction listing

Below are the instructions available to the programmer:

MNEMONIC	OPCODE	BRIEF
NOP	0x00	No-operation
MOV R0/M, R1	0x01	Move register to memory or register
ADD R0, R1, R2	0x02	Add R1 and R2 and copy result to R0
SUB R0, R1, R2	0x03	Subtract R1 and R2 and copy the result to R0
RESERVED	0x04	Reserved, do not use
RESERVED	0x05	Reserved, do not use
RESERVED	0x06	Reserved, do not use